



**Series  
T753-500**

**Phase Control Press-Pack  
Thyristor  
Type T753-500**

Distributed amplifying gate  
Designed for traction and industrial applications

Maximum mean on-state current	<b>I<sub>TAV</sub> 500 A</b>					
Maximum repetitive peak off-state and reverse voltage	<b>U<sub>DRM</sub> 5000 ÷ 6000 V</b>					
Turn-off time	<b>t<sub>q</sub> 500; 630 µs</b>					
U <sub>DRM</sub> , U <sub>RRM</sub> , V	5000	5200	5400	5600	5800	6000
Voltage code	50	52	54	56	58	60
T <sub>vj</sub> , °C	- 60 ÷ 125					

**MAXIMUM ALLOWABLE RATINGS**

Symbols and parameters		Units	T753-500	Conditions
I <sub>TAV</sub>	Mean on-state current	A	500 700 810	T <sub>c</sub> =92 °C, T <sub>c</sub> =70 °C, T <sub>c</sub> =55 °C, 180° half-sine wave, 50 Hz
I <sub>TRMS</sub>	RMS on-state current	A	785	T <sub>c</sub> =92 °C
I <sub>TSMS</sub>	Surge on-state current	kA	11,5 12,5	T <sub>vj</sub> =125°C T <sub>vj</sub> =25°C
I <sup>2</sup> t	Limiting load integral	kA <sup>2</sup> s	661 781	T <sub>vj</sub> =125°C T <sub>vj</sub> =25°C
U <sub>DRM</sub> , U <sub>RRM</sub>	Repetitive peak off-state and reverse voltage	V	5000÷6000	T <sub>j min</sub> ≤T <sub>vj</sub> ≤T <sub>jM</sub> 180° half-sine wave, 50 Hz Gate open
U <sub>DSM</sub> , U <sub>RSMS</sub>	Non-repetitive peak off-state and reverse voltage	V	5100÷6100	T <sub>j min</sub> ≤T <sub>vj</sub> ≤T <sub>jM</sub> 180° half-sine wave tp=10 ms, Single pulse Gate open
(dI <sub>T</sub> /dt) crit	Critical rate of rise of on-state current : non - repetitive repetitive	A/µs	630 320	T <sub>vj</sub> =125°C ; U <sub>D</sub> =0,67 U <sub>DRM</sub> , Gate pulse : 10V, 5 Ω, 1µs rise time, 10 µs
U <sub>RGMS</sub>	Peak reverse gate voltage	V	5	T <sub>j min</sub> ≤T <sub>vj</sub> ≤T <sub>jM</sub>
T <sub>stg</sub>	Storage temperature	°C	-60÷80	
T <sub>vj</sub>	Junction temperature	°C	-60÷125	

**CHARACTERISTICS**

U <sub>TM</sub>	Peak on-state voltage	V	2,7	T <sub>vj</sub> =25°C, I <sub>TM</sub> =3,14 I <sub>TAV</sub>
U <sub>T(TO)</sub>	Threshold voltage	V	1,4	T <sub>vj</sub> =125°C
R <sub>T</sub>	On-state slope resistance	mΩ	1,17	1,57 I <sub>TAV</sub> < I <sub>T</sub> <4,71 I <sub>TAV</sub>
I <sub>DRM</sub> I <sub>RRM</sub>	Repetitive peak off-state and reverse current	mA	100 100	T <sub>vj</sub> =125°C, U <sub>D</sub> = U <sub>DRM</sub> U <sub>R</sub> = U <sub>RRM</sub>

## CHARACTERISTICS

Symbols and parameters		Units	T753-500	Conditions
I <sub>L</sub>	Latching current	A	6	Tvj=25°C, UD=12V Gate pulse : 10V, 5Ω, 1 µs rise time, 10µs
I <sub>H</sub>	Holding current	A	1,0	Tvj=25°C, UD=12V, Gate open
U <sub>GT</sub>	Gate trigger direct voltage	V	2,5 5,0	Tvj=25°C, Tvj=-60°C UD=12V
I <sub>GT</sub>	Gate trigger direct current	A	0,3 0,85	Tvj=25°C, Tvj=-60°C
U <sub>GD</sub>	Gate non-trigger direct voltage	V	0,25	Tvj=125°C, UD = 0,67 U <sub>DRM</sub> Direct gate current
I <sub>GD</sub>	Gate non-trigger direct current	mA	10	
t <sub>gd</sub>	Delay time	µs	4,0	Tvj=25°C, UD=500V ITM = 500 A
t <sub>gt</sub>	Turn-on time	µs	12	Gate pulse : 10V, 5Ω, 1 µs rise time, 10µs
t <sub>q</sub>	Turn-off time	µs	500÷630	Tvj=125°C, ITM=500 A di <sub>R</sub> /dt =10 A/µs, UR=100V UD = 0,67 U <sub>DRM</sub> du <sub>D</sub> /dt=50 V/µs
Q <sub>rr</sub>	Recovered charge	µC	2800	Tvj=125°C, ITM=500 A dir/dt=10 A/µs, UR=100V
t <sub>rr</sub>	Reverse recovery time	µs	38	
I <sub>rrm</sub>	Peak reverse recovery current	A	150	
(d <sub>UD</sub> /dt) <sub>crit</sub>	Critical rate of rise of off-state voltage	V/µs	500 1000	
R <sub>thjc</sub>	Thermal resistance junction to case	°C/W	0,023	Direct current, double side cooled

## ORDERING

	<b>T</b>	<b>753</b>	<b>500</b>	<b>56</b>	<b>7</b>	<b>1</b>	
	<b>1</b>	<b>2</b>	<b>3</b>	<b>4</b>	<b>5</b>	<b>6</b>	

1. Phase control thyristor
2. Design version.
3. Mean on-state current, A.
4. Voltage code (56=5600 V).
5. Critical rate of rise of off-state voltage ( $6 \geq 500 \text{ V/}\mu\text{s}$ ,  $7 \geq 1000 \text{ V/}\mu\text{s}$ ).
6. Group of turn-off time ( $\text{du}_D/\text{dt}=50 \text{ V/}\mu\text{s}$ ,  $C_2 \leq 630 \mu\text{s}$ ,  $1 \leq 500 \mu\text{s}$ ).

